

Intel® Chipset Device Software

10.1.18383.8213

Release Notes

May 7, 2020

Microsoft Windows* 10 x86(only for Intel® Atom™ / Celeron®/ Pentium® Processor)
Microsoft Windows* 10 x64
Microsoft Windows* Server 2019 x64
Microsoft Windows* Server 2016 x64
Microsoft Windows* Server 2012R2 x64

INFORMATION IN THIS DOCUMENT IS PROVIDED IN CONNECTION WITH INTEL PRODUCTS. NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. EXCEPT AS PROVIDED IN INTEL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, INTEL ASSUMES NO LIABILITY WHATSOEVER AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO SALE AND/OR USE OF INTEL PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

A "Mission Critical Application" is any application in which failure of the Intel Product could result, directly or indirectly, in personal injury or death. SHOULD YOU PURCHASE OR USE INTEL'S PRODUCTS FOR ANY SUCH MISSION CRITICAL APPLICATION, YOU SHALL INDEMNIFY AND HOLD INTEL AND ITS SUBSIDIARIES, SUBCONTRACTORS AND AFFILIATES, AND THE DIRECTORS, OFFICERS, AND EMPLOYEES OF EACH, HARMLESS AGAINST ALL CLAIMS COSTS, DAMAGES, AND EXPENSES AND REASONABLE ATTORNEYS' FEES ARISING OUT OF, DIRECTLY OR INDIRECTLY, ANY CLAIM OF PRODUCT LIABILITY, PERSONAL INJURY, OR DEATH ARISING IN ANY WAY OUT OF SUCH MISSION CRITICAL APPLICATION, WHETHER OR NOT INTEL OR ITS SUBCONTRACTOR WAS NEGLIGENT IN THE DESIGN, MANUFACTURE, OR WARNING OF THE INTEL PRODUCT OR ANY OF ITS PARTS.

Intel may make changes to specifications and product descriptions at any time, without notice. Designers must not rely on the absence or characteristics of any features or instructions marked "reserved" or "undefined". Intel reserves these for future definition and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to them. The information here is subject to change without notice. Do not finalize a design with this information.

The products described in this document may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Contact your local Intel sales office or your distributor to obtain the latest specifications and before placing your product order.

Code names featured are used internally within Intel to identify products that are in development and not yet publicly announced for release. Customers, licensees and other third parties are not authorized by Intel to use code names in advertising, promotion

The Intel® Chipset Device Software may contain design defects or errors described as "known issues" which may cause the product to deviate from expected performance. Current characterized known issues are documented in this update.

Intel and the Intel logo are trademarks of Intel Corporation in the U.S. and other countries.

*Other names and brands may be claimed as the property of others.

Copyright © 2019, Intel Corporation. All rights reserved.

Revision History

Date	Driver Revision	Description	Build Number
May 7, 2020	10.1.18383.8213	PV	18383.8213
Apr 15, 2020	10.1.18340.8203	PV	18340.8203
Feb 4, 2020	10.1.18295.8201	PV	18295.8201
Jan 7, 2020	10.1.18267.8194	PV	18267.8194
Jan 3, 2020	10.1.18263.8193	PV	18263.8193

Intel® Chipset Device Software Support

Driver version	Minor version	Description (supported products)

10.1.33	3	Intel® 400 Series Chipset Family
10.1.31	2	Intel® 400 Series Chipset Family
10.1.30	3	Intel(R) Host Bridge/DRAM Registers
10.1.28	2	Intel(R) LPC Controller Intel(R) P2SB Intel(R) PMC Intel(R) SPI (flash) Controller Intel(R) PCI Express Root Port Intel(R) Shared SRAM
10.1.27	4	Intel(R) On-Package Platform Controller Hub
10.1.22	3	Intel® 495 Series Chipset Family
10.1.20	3	10 th Gen Intel® Core®
10.1.16	7	Intel(R) 300 Series Chipset Family
10.1.16	7	Intel(R) C240 Series Chipset Family
10.1.15	6	Mobile 8 th Gen Intel® Core™ processor family
10.1.14	8	8th Gen Intel(R) Core™
10.1.13	3	Intel(R) Celeron(R)/Pentium(R) Processor
10.1.12	3	Intel(R) 495 Series Chipset family
10.1.11	4	Intel(R) 200 series chipset family
10.1.11	4	Intel(R) 300 series chipset family
10.1.10	5	Intel(R) Xeon(R) processor E3-1200 v6 product family
10.1.10	5	7th Generation Intel(R) Core(TM) processor family
10.1.7	3	Intel(R) Xeon(R) processor E3-1500 v5 product family
10.1.7	3	Intel(R) Xeon(R) processor E3-1200 v5 product family

10.1.7	3	6th Generation Intel(R) Core(TM) processor family
10.1.6	2	Intel(R) 100 Series chipset
10.1.6	2	Intel(R) C230 series chipset family
10.1.5	3	7th Generation Intel(R) Core(TM) processor family
10.1.5	3	6th Generation Intel(R) Core(TM) processor family

New Features

Features Added in Build 10.1.18383.8213	
ID	Description
1	[10.1.12.3] certified for 20h1
2	[10.1.14.8] certified for 20h1
3	[10.1.20.3] certified for 20h1
4	[10.1.22.3] certified for 20h1
5	[10.1.27.4] certified for 20h1
6	[10.1.28.2] certified for 20h1
7	[10.1.30.3] certified for 20h1
8	[10.1.31.2] certified for 20h1
9	[10.1.33.3] certified for 20h1
Features Added in Build 10.1.18340.8203	
ID	Description
1	none
Features Added in Build 10.1.18295.8201	
ID	Description
1	[10.1.28.2] made public
2	[10.1.5.3] certified for 19h2
3	[10.1.10.5] certified for 19h2
4	[10.1.14.8] certified for 19h2
5	[10.1.30.3] added 9B54, and 9B64. Certified for 19h2
Features Added in Build 10.1.18267.8194	
ID	Description
1	[10.1.20.3] added
2	[10.1.22.3] added
Features Added in Build 10.1.18263.8193	
ID	Description
1	[10.1.30.2] Made public, certified.

2	[10.1.31.2] Made public, certified, updated SKUs for LPC Controller devices.
3	[10.1.33.3] Updated SKUs for LPC Controller devices.

Installer update

Changes in Installer version 3.1.7.127	
ID	Description
1	Make extension infs install by installer - change logic to install many infs for single HWID with restriction: inf files must be from different folders.
Changes in Installer version 3.1.7.121	
ID	Description
1	Removed supported platform list from readme
2	Programs and Features (ARP) entry for Chipset INF will always have English product name. Before name was localized during installation and looked bad when OS changed display language.
Changes in Installer version 3.1.7.120	
ID	Description
1	Readme.txt shows Package version and Installer version
Changes in Installer version 3.1.7.118	
ID	Description
1	Update MUP.xml file with error code 350 mapped to REBOOT_UPDATE_PENDING
Changes in Installer version 3.1.7.117	
ID	Description
1	Added Fix for CVE-2019-14596
Changes in Installer version 3.1.7.107	
ID	Description
1	Added -ignoreId switch
2	Updates in custom action logging
Changes in Installer version 3.1.7.100	
ID	Description
1	fixed NDA readme file installed by Public package, now Public package installs correct readme file.
Changes in Installer version 3.1.7.99	

ID	Description
1	Fixed "the messy code is displayed during install chipset on Arabic OS"
Changes in Installer version 3.1.7.98	
ID	Description
1	CSVer.dll removed from package
2	Security fixes
3	MUP extract and extract functions fixed
4	Added Add Remove Programs entry function
5	Added uninstall functionality, does not uninstall drivers – only package contents (license, readme, registry keys, AddRemove programs entry)
6	Installation fails with known error code on unsupported platform
Changes in Installer version 3.1.7.90	
ID	Description
1	MUP packages have DriverFiles folder complying to MUP path requirements - \production\ <os_name>-<os_arch> </os_name>-<os_arch> Also supports Windows10S requirement (raw driver files in package)
2	Supports Windows10S requirement (raw driver files in package)
3	MUP 2.4.5 compliant
4	Please note WiX framework used to build installer got updated to 3.11.1 which contains security fixes
Changes in Installer Version 3.1.7.89	
ID	Description
1	Tweaked previous fix implementation for: Thunderbolt [TBT] power flow issue while re-applying PCIe controller inbox – added devcon disable/enable functionality Before inf installation devcon is used to: <ul style="list-style-type: none"> - disable root TBT devices (device IDs as found in table in HSD – device parent must not be a TBT device), - task is scheduled in Task Scheduler, - task runs on OS startup and uses devcon to enable the same root TBT device, then the task removes itself. –devcon switch, usage example –devcon c:\tools\devcon.exe Note: without an externally supplied devcon the function won't work.
Changes in Installer Version 3.1.7.88	
ID	Description
1	Thunderbolt [TBT] power flow issue while re-applying PCIe controller inbox – added devcon disable/enable functionality <ul style="list-style-type: none"> - Before inf installation devcon is used to disable TBT devices , then a task is scheduled in Task Scheduler; task runs on OS startup and uses devcon to enable the same devices twice with timeout in-between, then the task removes itself.

- `-devcon` switch, usage example `-devcon c:\tools\devcon.exe`
without an externally supplied devcon the function won't work
- `-devcontimeout` switch, usage example `-devcontimeout 10`
sets how long to wait in seconds in-between enables

Changes in Installer Version 3.1.7.86

ID	Description
1	Added <code>-exclude</code> switch <ul style="list-style-type: none">- <code>'-exclude usb'</code> will not install any inf file that has USB in it's name