



BIOS Update Release Notes

PRODUCTS: DG43NB, DP43TF (Standard BIOS)

BIOS Version 0060

About This BIOS:

- September 12, 2008
- NBG4310H.86A.0060.2008.0912.1124
- GBE firmware bin version: ich10r_82567v_a2
- PXE Option Rom file: BA1322L2.LOM
- ME Version: 1087 SKU4
- MEBx Version:5.0.5.0003
- Vbios:1667

New Fixes/Features:

- Fixed DTM PS2 keyboard/mouse resource allocated not suitable in Windows when they are not connected.
- Fixed issue to display the correct CPU ratio on CPU Q9650.
- Fixed issue where the recovery function does not work.
- Fixed RTC issue where a change to the system date/time may cause the system hang.
- Removed some POST codes to prevent bios setup becoming a white screen.
- Fixed issue where connecting the PS2 keyboard and mouse at the same time, the complex key CTRL-P will not work.
- Fixed the system hang at 0x92.
- Fixed issue where PS2 mouse and keyboard can not function in Windows Vista*.
- Fixed issue where PS2 mouse and USB keyboard on system caused S4 resume to hang with code 9B or BA.
- Fixed Intel® Integrator Toolkit issue: BootCallbackRoutine()
- Fixed S3 bounce issue.
- Cleared Intel® ME wake up status before entering sleep states.
- Updated the latest NVRAM driver.
- Fixed unexpectedly OEMID in XSDT being modified to cause SLP2.0 activation failure.
- Synced AMI library to v4.6.3.1.
- Fixed PCIE scan algorithm to solve some VGA devices that cannot be detected when behind on-device bridge.
- Adjusted the size of FV_BB & FV_MAIN.
- Updated VBIOS to v1676.
- Fixed issue where system cannot enter PXE if a SCSI card is plugged into PCI slot.
- Removed GetVariable in timer event function, which might cause NVRAM operation miss.
- Fixed issue with the buffer used to get Intel® ME string.
- Fixed yellow bang with PS2 keyboard/mouse if there is no PS2 keyboard/mouse plugged in.

*Other names and brands may be claimed as the property of others.

- Fixed minor NVRAM issue.
- Forbid NVRAM compact at runtime.
- Reserved 4K bytes for NVRAM compact.
- Fixed system hang at "Power Saver" option when EIST is disabled.
- Reserved SMBus IO address and forbid PCI driver to modify it.
- Double confirmed if clkgen's value has been filled in.
- Added boundary condition for clkgen programming loop.
- Cleaned up unused entries in resource map.
- Fixed system hangs at entering S5.
- Skipped sending ASF message if system resumes from S3.
- Modified JMicron* PEI driver and FFS link to solve PATA recovery issue.
- Fixed issue where installing EFI OS takes too long.
- Added a patch for automatic UEFI boot without pressing F10.
- Added a special SLP_SMI handler for S5 for Intel® Integrator Toolkit.
- Introduced NvRam checksum mechanism.
- Disabled IRQ during NvRam writing process.
- Added break while-loop in NVRAM compact if flash erasing/writing fails over 10 times.
- Fixed 8 GB performance issue.
- Restored NVRAM address to normal NVRAM on DXE.
- Adjusted checksum sample range.
- Refined USB emulation function to make default boot to SATA0 when BIOS first boot.
- Removed MFGMODE ICT DATA and add 0x290000 backup data.
- Restored SPI registers on S3 resume.
- Set value to NvRam area for sata0 boot.
- Implemented complete NVRAM solution.
- Set SMBIOS typel6 max memory capacity = 8GB.
- Forbid JMicron register IDE recovery ppi.
- Removed NVRAM backup - only do restore after NVRAM crashed.
- Adjusted IFLASH shutdown wakeup time.
- Removed NVRAM Checksum recovery.
- Added NVRAM auto recovery function.
- Fixed NVRAM crash issue:
 - Backup normal one to backup on first boot.
 - Automatically switch NVRAM to backup if normal one is broken.
 - Recover NVRAM while entering DXE phase.
- Updated Intel® ME bin for default Intel® ME setting.
- Updated JMicron IDE Controller driver to fix PXE issue.
- Adjusted checksum sample range to avoid calculating MfgMode variable.
- Restored CSM binary and SMM workaround for A20TEST.
- Added NvRam backup solution.
- Added CMOS index reservation while entering SMM mode.
- Disabled UHCI root hub 8ms SMI polling to avoid flash being crashed.
- Refined SIO PEI code to save more space.
- Enabled KBC burst and disable Gate#20 switch in SIO.
- Disabled CheckLoadDefault module.
- Patched DMI data shift 0xf issue.
- Removed DxeStartupX64.ffs from FV_MAIN to save about 19XKB space.
- Set Chassis Intrusion Lock to default disabled.

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- Updated processor support.
- Removed NvRam checksum error message, and skip delay.
- Disabled USB SMI while processing flash write to improve NvRam quality.
- Updated Descriptor_Region.bin by USI.
- Updated PXE rom.
- Disabled VpdBackup module.
- Replaced Flash.
- Set Local Firmware update to Enable.Set MEManuf Test Counter to 0.Set ME FW offset 400 to 01.
- Reprogrammed CPU MTRR if 8 GB DIMM populated.
- Updated CSM binary to rev.47.

BIOS Version 0040

About This BIOS:

- July 07, 2008
- NBG4310H.86A.0040.2008.0707.1545
- GBE firmware bin version: ich10r_82567v_a2
- PXE Option Rom file: BA1310L2.LOM
- ME Version: 1087 SKU4
- MEBx Version:5.0.5.0003
- Vbios:1637

New Fixes/Features:

- Initial production BIOS release

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