



Intel® Desktop Board D915GLVG Specification Update

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The Intel® Desktop Board D915GLVG may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

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The Intel® desktop board D915GLVG may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

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REVISION HISTORY

Date of Revision	Version	Description
May 4, 2005	-001	This document is the first Specification Update for the Intel® Desktop Board D915GLVG.
July 2006	-002	Added Specification Changes 1, Erratum 1 and updated the General Information table.



PREFACE

This document is an update to the specifications contained in the *Intel® Desktop Board D915GLVG Technical Product Specification* (Order number D18285). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

Refer to the *Intel® Pentium® 4 Processor Specification Update* (Order number 249199) for specification updates concerning the Intel Pentium 4 processor and that may apply to the desktop board D915GLVG. Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® 82915GL* (Order Number 301468) for specification updates concerning the 82915GL MCH Controller and that may apply to the desktop board D915GLVG. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® IO Controller Hub 6 (ICH6) Family Specification Update* (Order Number 301474) for specification updates concerning the 82801FB I/O Controller Hub and that may apply to the desktop board D915GLVG. Unless otherwise noted in this document, it should be assumed that any ICH 6 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Nomenclature

Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

Errata are design defects or errors. Characterized errata may cause the desktop board D915GLVG's behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

Specification Clarifications describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

**Specification Update for the
Intel[®] Desktop Board D915GLVG**



GENERAL INFORMATION

Basic Desktop Board D915GLVG Identification Information

AA Revision	BIOS Revision	Notes
C98734-301	VG91510A.86A.0021	1-2
C98734-302	VG91510A.86A.0021	1-2
C98734-303	VG91510A.86A.0042	1-2
C98734-304	VG91510A.86A.0048	1-2
C96367-303	VG91510A.86A.0021	1-2
C96367-304	VG91510A.86A.0021	1-2
C96367-305	VG91510A.86A.0042	1-2
C96367-306	VG91510A.86A.0048	1-2

NOTES:

1. The AA number is found on a small label on the component side of the board.
2. The 82910GL Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers
82915GL GMCH	C2	SL8CK
82801FB ICH6	B2	SL7Y5



Summary Table of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board D915GLVG. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

CODES USED IN SUMMARY TABLE

Doc:	Document change or update that will be implemented.
Plan Fix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
No Fix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

NO.	PLANS	SPECIFICATION CHANGES
1	Doc	Changes to Section 2.8.2.2, Add-in Card Connectors.
NO.	PLANS	ERRATA
1	Plan Fix	The placement of capacitors behind the PCI Express* x1 connector slot may prohibit some PCI Express x1 Add-in cards from properly connecting to the motherboard



SPECIFICATION CHANGES

The Specification Changes listed in this section apply to the *Desktop Board D915GLVG Technical Product Specification* (Order Number D18285). All Specification Changes will be incorporated into a future version of that specification.

- The following note will be added to the TPS in Section 2.8.2.2 Add-in Card Connectors:***

NOTE

Capacitors required for the audio circuitry are located within the ATX keep-out area near the PCI Express x1 slot, and may limit the use of some PCI Express x1 cards.*



ERRATA

1. The placement of capacitors behind the PCI Express* x1 connector slot may prohibit some PCI Express x1 add-in cards from properly connecting to the motherboard.

PROBLEM: Due to the design and placement of the audio circuitry on this motherboard, it was necessary to position capacitors in the keep-out zone behind the PCI Express* x1 connector slot.

IMPLICATION: Some PCI Express x1 add-in cards may not fully connect to the PCI Express x1 connector slot, due to interference between capacitors in the keep-out zone and I/O connectors on the add-in card that are located on the I/O bracket, in line with the edge fingers on the card.

WORKAROUND: None

STATUS: MAY BE FIXED IN A LATER HARDWARE REVISION.